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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/659,505	09/10/2003	Atsuo Fukui	S004-5107	3598

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EXAMINER

NGUYEN, MATTHEW VAN

ART UNIT PAPER NUMBER

2838

DATE MAILED: 03/25/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/659,505

Applicant(s)

FUKUI, ATSUI

Examiner

MATTHEW V. NGUYEN

Art Unit

2838

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 September 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-3 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1 and 2 is/are allowed.
- 6) ☒ Claim(s) 3 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☒ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

1. The disclosure should be carefully reviewed and ensure that any and all grammatical, idiomatic, and spelling or other minor errors are corrected.
2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 3 is rejected under 35 U.S.C. 102(e) as being anticipated by Horie (U.S. Pat. No. 6,559,626).

With regard to claims 3, Horie (i.e, Fig. 2) shows a voltage regulator comprising a reference voltage source (at terminal 8) for generating a reference voltage (vR1), a voltage dividing circuit (R1, R2) for dividing an output voltage, a feedback voltage terminal (midpoint of R1, R2) to which a voltage obtained by dividing the output voltage is outputted, an error amplifier (12) to which the reference voltage and a voltage from the feedback voltage terminal are inputted, a first transistor (Q1) of a first conductivity type, that is connected in series between the voltage dividing circuit and an input power source terminal (2), and an overcurrent limiting circuit (13) for outputting a signal for controlling the first transistor based on an output of the error amplifier, that including a differential

pair (Q9, Q10) for outputting the signal for controlling the first transistor based on a signal inputted to the error amplifier.

3. Claims 1 and 2 are allowable over prior art.

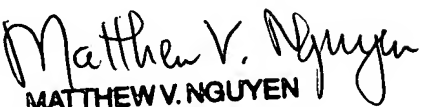
4. The following is an examiner's statement of reasons for allowance to claims 1 and 2: none of prior art of record taken alone or in combination shows a voltage regulator comprising an overcurrent limiting circuit including a second transistor, a first resistor, a third transistor, a second resistor, a fourth transistor and a differential pair along with specific electronic connections among those elements and particular functions thereof as recited in the claims of the instant application.

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Lonergan (U.S. Pat. No. 4,841,219), Matsushita (U.S. Pat. No. 6,509,723) and Miyazaki (U.S. Pat. No. 6,608,520) also disclose voltage regulators each of which comprises an error amplifier and an overcurrent limiting circuit.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew V. Nguyen whose telephone number is (571) 272-2081.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-2800.


MATTHEW V. NGUYEN
PRIMARY EXAMINER